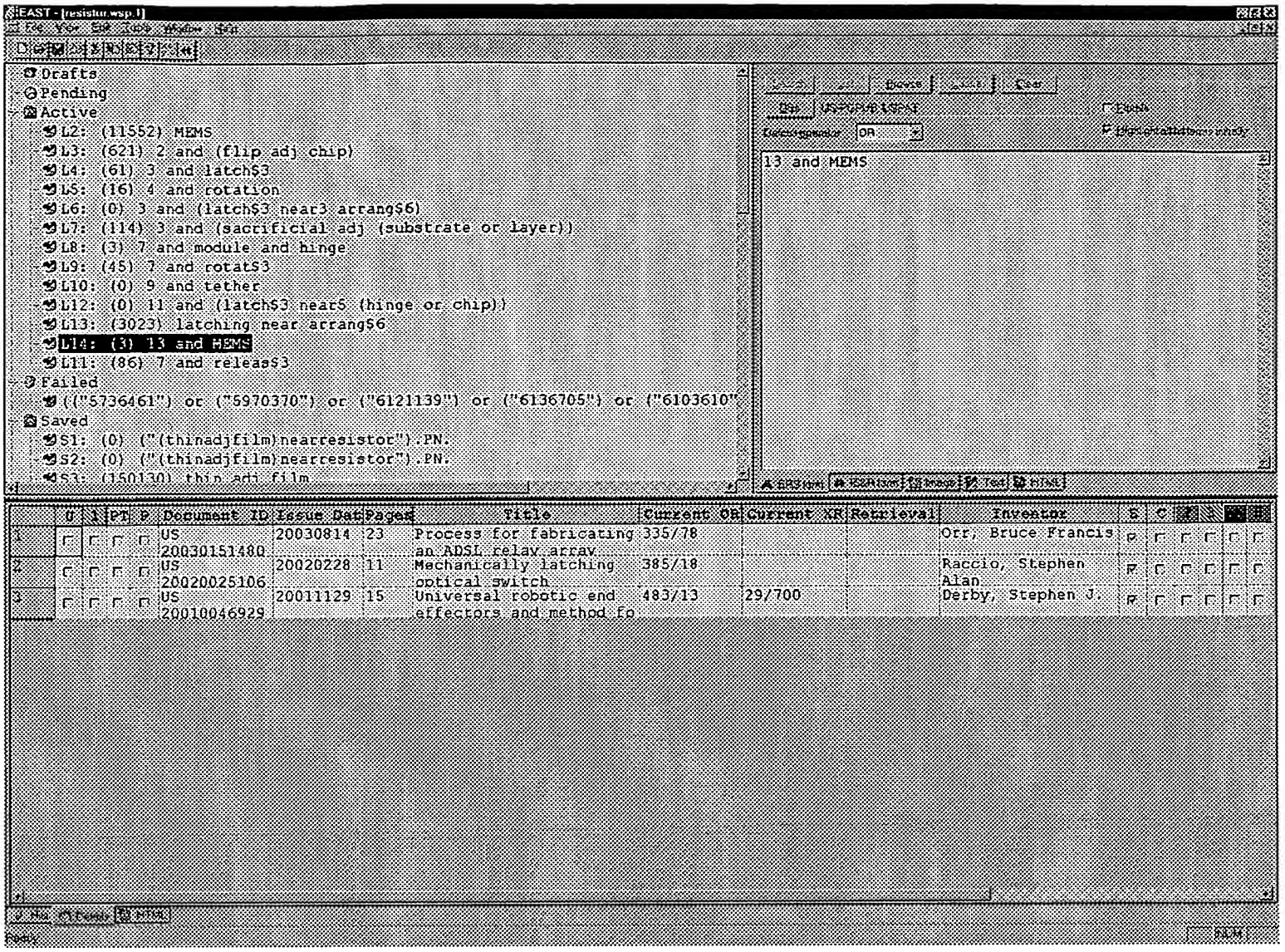
EAST - [resistor.wsp.1]
Drafts
Pending
Active
L2: (11552) MEMS
L3: (621) 2 and (flip adj chip)
L4: (61) 3 and latch$3
L5: (16) 4 and rotation
L6: (0) 3 and (latch$3 near3 arrang$6)
L7: (114) 3 and (sacrificial adj (substrate or layer))
L8: (3) 7 and module and hinge
L9: (45) 7 and rotat$3
L10: (0) 9 and tether
L12: (0) 11 and (latch$3 near5 (hinge or chip))
L13: (3023) latching near arrang$6
L14: (3) 13 and MEMS
L11: (86) 7 and release$3
Failed
(("5736461") or ("5970370") or ("6121139") or ("6136705") or ("6103610"
Saved
S1: (0) ("(thinadjfilm)nearresistor").PN.
S2: (0) ("(thinadjfilm)nearresistor").PN.
13 and MEMS
Search
Clear
Text
HTML
0 | 1 | PT | P | Document ID | Issue Dat | Pages | Title | Current OR | Current XR | Retrieval | Inventor | S
1 | US 20030151480 | 20030814 | 23 | Process for fabricating an ADSL relay array | 335/78 | | | Orr, Bruce Francis
2 | US 20020025106 | 20020228 | 11 | Mechanically latching optical switch | 385/18 | | | Raccio, Stephen Alan
3 | US 20010046929 | 20011129 | 15 | Universal robotic end effectors and method fo | 483/13 | 29/700 | | Derby, Stephen J.
Ready
NUM